

REMARKS

Status of Claims:

Claims 2 and 10-13 have been canceled. Claims 1 and 3-9 remain for examination.

Acknowledgment of Priority Document:

The examiner filed to check the box "all" in box 12a indicating that all priority documents have been received. Appropriate correction is requested.

Title:

A new title of the invention has been submitted herewith consistent with Paragraph 1 of the office action.

Objections to Specification:

Various objections to the specification have been made in paragraph 2 of the office action. The specification has been amended to remove the basis for these objections. The term "short-through" has been **replaced** throughout the specification by "shoot-through".

Claim Objections:

The objections to the claims set forth in paragraphs 3 and 4 have been attended to in the instant amendment.

Rejection under Sec. 112, Par. 1.

The examiner has rejected claim 3 under 35 U.S.C. § 112, Par. 1. Applicant has amended claim 3 to be exactly consistent with the written description found on page 12, lines 3-16 of the originally filed application. It is thus submitted that claim 3, as amended is fully supported by the application as originally filed.

Prior Art Rejections:

Claim 1, 5 and 7-13 stand rejected under 35 U.S.C. § 102 as anticipated by Fukasawa. Further, claims 2-4 and 6 stand rejected under 35 U.S.C. § 103 as obvious over Fukasawa in view of Toyama.

The examiner's rejections are respectfully traversed.

Applicant has amended claim 1 to incorporate therein the limitations of claim 2 and has further specified that the suppressing circuit includes a logic circuit receiving an enable signal in a first state from one of said first circuit and second circuit which is ON for suppressing said shoot-through current between said first circuit and second circuit when the other of said first circuit and second circuit is OFF, and receiving an enable signal in a second state for enabling signal transfer between said first and second circuits when each of said first and second circuits are ON. These limitations are not disclosed nor made obvious by Fukasawa taken singly or in combination with Toyama.

In a somewhat similar fashion, claim 4 has been placed in independent form and further amended to include a logic circuit receiving an enable signal in a first state from one of said first circuit and second circuit which is ON for suppressing said leakage current between said first circuit and second circuit when the other of said first circuit and second circuit is OFF, and receiving an enable signal in a second state for enabling signal transfer between said first and second circuits when each of said first and second circuits are ON

Claims 5-8 have been amended adopting the examiner's suggestion as stated in par. 4 of the office action. These claims are dependent claims and are deemed patentable at least by virtue of their dependency.

Claims 10-13 have been cancelled to expedite prosecution of the instant application.

Conclusion:

Applicant believes that the present application is now in condition for allowance. Favorable reconsideration of the application as amended is respectfully requested.

The Examiner is invited to contact the undersigned by telephone if it is felt that a telephone interview would advance the prosecution of the present application.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741. If any extensions of time are needed for timely acceptance of papers submitted herewith, Applicant hereby petitions for such extension under 37 C.F.R. §1.136 and authorizes payment of any such extensions fees to Deposit Account No. 19-0741.

Respectfully submitted,

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